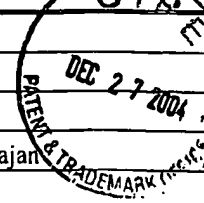


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				Application Number	10/648,591
				Filing Date	August 25, 2003
				First Named Inventor:	Luke S. Theogarajan
				Art Unit	2816
				Examiner Name	Cox, Cassandra F.
				Attorney Docket Number	42P10986C
Sheet	1	of	1		



NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
CFL	1.	SANCHEZ, HECTOR et al., "A Versatile 3.3/2.5/1.8-V CMOS I/O Driver Built in a 0.2-μm, 3.5-nm Tox, 1.8-V CMOS Technology", IEEE Journal of Solid-State Circuits, Vol. 34, No. 11, November 1999.	
CFL	2.	CONNOR, JOHN et al., "Dynamic Dielectric Protection For I/O Circuits Fabricated in a 2.5V CMOS Technology Interfacing to a 3.3V LVTTTL Bus", 1997 Symposium on VLSI Circuits Digest of Technical Papers.	
CFL	3.	SINGH, GAJENDRA et al., "High-Voltage-Tolerant I/O Buffers with Low-Voltage CMOS Process", IEEE Journal of Solid-State Circuits, Vol. 34, No. 11, November 1999.	

Examiner Signature		Date Considered	January 31, 05
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English translation is attached.

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